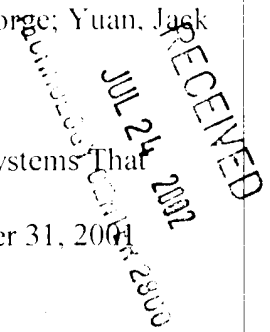


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Guterman, Daniel C.; Harari, Eliyahou; Samachisa, George; Yuan, Jack H.  
Assignee:  
Title: Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements  
Application No.: 10/02/696 Filing Date: October 31, 2001  
Examiner: 10/002, 696 Group Art Unit: 2812  
Docket No.: M-12336 US



San Francisco, California  
March 20, 2002

COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying form PTO-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

EXPRESS MAIL LABEL NO:

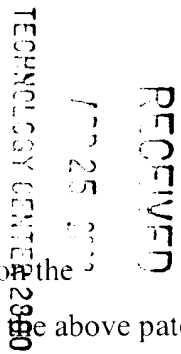
EV 029354407 US

Respectfully submitted,

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